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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application / Conf. No.	10/086,129 / 2232
Mail Stop: AMENDMENT		Filing Date	February 28, 2002
		First Named Inventor	David P. Schultz
		Examiner Name	James C. Kerveros
		Art Unit	2133
Express Mail Receipt No.		Patent No.	
Total Number of Pages in This Submission		Attorney Docket Number	X-1069 US

ENCLOSURES *(check all that apply)*

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Date	February 27, 2006	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D. C. 20231 on this date:

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Signature		Date February 27, 2006

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X-1069 US
10/086,129

PATENT
Conf. No.: 2232



IN THE UNITED STATES PATENT OFFICE

Applicant: David P. Schultz
Assignee: Xilinx, Inc.
Title: Method and System for Flexibly Nesting JTAG TAP Controllers
for FPGA-Based System-On-Chip (SoC)
Serial No.: 10/086,129 File Date: 2/28/2002
Examiner: James C. Kerveros Art Unit: 2133
Docket No.: X-1069 US Conf. No.: 2232

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION AFTER RCE

Dear Sir:

In response to the rejection in the First Office Action after an RCE mailed from the Patent Office on November 22, 2005, please amend the claims as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 8 of this paper.